



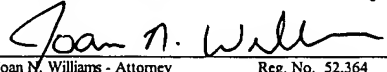
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Applicant : Luan Tran  
Serial No. : 10/649,507  
Filed : August 26, 2003  
Title : **SELECTIVE POLYSILICON STUD GROWTH**  
Docket : MIO 0033 V3/40509.258

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Joan N. Williams - Attorney Reg. No. 52,364

Sir:

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UNDER 37 CFR §§ 1.56, 1.97, AND 1.98

Applicant submits herewith patents, publications, and other information of which he is aware, which he believes may be material, as defined in 37 CFR §1.56(b), to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR §1.56(a). While the information referred to in this Information Disclosure Statement may be material pursuant to 37 CFR §1.56(b), the filing of this Information Disclosure Statement is not intended to, pursuant to 37 CFR §1.97(h), constitute an admission that any patent, publication, or other information referred to is, or is considered to be, material to the patentability of this invention. Further, pursuant to 37 CFR §1.97(g), the filing of this Statement should not be construed as a statement that a search has been made or that no other material information exists.

This Information Disclosure Statement is being filed within the period set forth in 37 CFR §1.97(b) because it is believed to be filed before the mailing date of a first office action on the merits.

The Office has waived the requirement pursuant to 37 CFR §1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003 and for all international applications

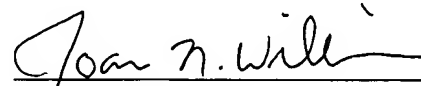
Serial No. 10/649,507

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that have entered the national stage under 35 USC §371 after June 30, 2003. Therefore, no copies of each cited U.S. patent and each cited U.S. patent application publication are enclosed, but the cited U.S. patents and the cited U.S. patent application publications are listed on PTO/SB/08A.

Respectfully submitted,  
DINSMORE & SHOHL LLP

By



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PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

**Complete if Known**

Application Number	10/649,507
Filing Date	August 26, 2003
First Named Inventor	Luan Tran
Art Unit	
Examiner Name	
Attorney Docket Number	MIO 0033 V3/40509.258

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US- 4,541,169 A	09/17/1985	Bartush	
		US- 5,145,802 A	09/18/1992	Tyson et al.	
		US- 5,302,844 A	04/12/1994	Mizuno et al.	
		US- 5,326,722 A	07/05/1994	Huang	
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		US- 5,747,844 A	05/05/1998	Aoki et al.	
		US- 5,780,343 A	07/14/1998	Bashir	
		US- 5,827,770 A	10/27/1998	Rhodes et al.	
		US- 5,901,092 A	05/04/1999	Tran	
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		US- 6,300,215 B1	10/09/2001	Shin	
		US- 6,429,529 B1	08/06/2002	Keeth	
		US-			
		US-			

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
		KR 2001008604	02/05/2001	Jun et al.		

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		Examiner Name			
Sheet	2	of	2	Attorney Docket Number	MIO 0033 V3/40509.258

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		KOGA ET AL., A 0.23 um Double Self-Aligned Contact Cell for Gigabit DRAMs With a Ge-Added Vertical Epitaxial Si Pad, 1996 IEEE, IEDM 96-589-IEDM 96-592	
		HADA ET AL., A Self-Aligned Contact Technology Using Anisotropical Selective Epitaxial Silicon For Giga-Bit DRAMs, IEEE, 1995 IEDM 95-665-IEDM 95-668	

Examiner Signature		Date Considered	
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